

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of
SHUNPEI YAMAZAKI et al

Serial No. 09/239,948

Filed: 01/29/1999

For: LASER PROCESSING METHOD,
METHOD FOR FORMING A FLASH
MEMORY, INSULATED GATE
SEMICONDUCTOR DEVICE AND
METHOD FOR FORMING THE SAME



Group Art Unit: 2811

Examiner: G. Munson

2811/IDS
#12/17/99
6-20-00

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INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

U.S. Patent No. 5,938,839 corresponds to Japanese Application No. 3-283981 and Japanese Application No. 3-290719, which is mentioned on page 14 of the specification.

U.S. Patent Nos. 5,424,244 and 5,849,043 correspond to Japanese Application No. 4-100479, which is mentioned on page 14 of the specification.

Japanese-Laid-Open No. 05-90159, published April 9, 1993 corresponds to Japanese Application No. 3-278705, which is mentioned on page 17 of the specification.

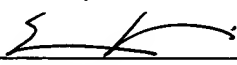
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U.S. Patent Nos. 5,495,121 and 5,716,871 correspond to Japanese Application No. 3-278706, which is mentioned on page 17 of the specification.

U.S. Patent No. 5,476,802 corresponds to Japanese Application Nos. 3-238710; 3-238711; and 3-238712, which is mentioned on page 20 of the specification.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



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